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Invited Talk:

How Artificial Intelligence is driving new compute architectures in an emerging memory centric world



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About the Speaker

Steve Pawlowski is Vice President of Advanced Computing Solutions at Micron Technology. He is responsible for defining and developing innovative memory solutions for the enterprise and high-performance computing markets. Prior to joining Micron in July 2014, Mr. Pawlowski was a Senior Fellow and the Chief Technology Officer for Intel's Data Center and Connected Systems Group. Mr. Pawlowski's extensive industry experience includes 31 years at Intel, where he held several high-level positions and led teams in the design and development of next-generation system architectures and computing platforms.

Mr. Pawlowski earned bachelor's degrees in electrical engineering and computer systems engineering technology from the Oregon Institute of Technology and a master's degree in computer science and engineering from the Oregon Graduate Institute. He also holds 58 patents.

Abstract

Artificial Intelligence solutions are creating a new focus on purpose built Computing. Neural networks are evolving at a significant rate to address the growing Complexity and volume of the data being created and analyzed. As one example, 3D Convolutional Neural Networks are one aspect of this evolution. Such a difference in the models will drive the need of more optimized computing and more, importantly, changes to the memory and storage architectures to improve the efficiency of the computing solution. This talk will discuss some of these changes, and the resultant opportunities in new memory/storage and computing solutions to come.