

e-Manufacturing & Design Collaboration 2014

Invited Speech:

Advanced Node Leadership Imperative



Mr. Mitch Lowe

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About the Speaker

Mr. Mitch Lowe has 35 years experience in the semiconductor and EDA industries, and is currently Vice President, Research and Development in Cadence's Chief Strategy Office. His current responsibilities include R&D level collaboration and enablement, with commercial IC Foundries, across all Cadence products, including analog design, implementation, and verification; digital implementation and signoff; mixed-signal design and implementation; physical verification; design-for-manufacturing (DFM); and design and analysis of complex PCBs and 3DIC integrated circuit (IC) packages. Mr. Lowe holds a BS degree in Computer Engineering from State University of New York at Stony Brook.

Abstract

This talk will describe how Cadence addresses the complexities and challenges of attaining a joint leadership role within the context of advanced node IC technologies, working closely with key technology and ecosystem partners, TSMC and ARM. The scope of the challenge spans many dimensions and requires readiness not only in fabrication technology, IP availability and EDA technologies, but must also be immediately deployed into the world's leading electronic product companies for high volume production of new devices that drive the advancing human condition.