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| **Invited Talk:** | **Benefits and Challenges of Deploying AI in Semiconductor Design and Manufacturing** |

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|  | **Rajesh Berigei**  *Technical Evangelist, MathWorks* |

**About the Speaker**

Rajesh Berigei is the Worldwide Semiconductor Manager at MathWorks, where he is the technical evangelist for the Communications, Electronics and Semiconductor industry on Analog Mixed-Signal, SerDes, SoC, FPGA, ASIC and Machine Learning workflows for the last two years. Prior to joining MathWorks, Rajesh headed the Worldwide System Design and EDA organization at Texas Instruments™ and before TI, was Director of System Design Technology at National Semiconductor. Rajesh has an MS in Electrical and Computer Engineering from UT-Austin and is working toward an MBA at UNC-Chapel Hill. Rajesh has more than 20 years of experience in semiconductor EDA, low power IC design, pre-silicon verification, post silicon validation and IC-package-board SI/PI analysis.

**Abstract**

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