

Invited Speech: **Achieving optimal efficiencies of vertical integrations along a horizontal supply chain**
Mr. John Cheng, Director of supply chain management, Verigy Ltd

About the Speaker



Mr. Cheng is Verigy's director of supply chain management. In this role, he is responsible for Verigy's worldwide fabless design houses and integrated device manufacturers (IDM) outsourcing. Mr. Cheng joined Agilent Technologies in 2004 bringing with him more than 18 years of semiconductor industry. He has worked specifically with design houses and OSAT providers for the last 10 years, witnessing firsthand the evolutionary growth of semiconductor outsourcing. Prior to Verigy, he held various management roles in engineering, marketing and sales. Mr. Cheng can be reached at john.cheng@verigy.com.

Abstract

There is no doubt that test is a critical node in the horizontal, disaggregated semiconductor manufacturing supply chain. From a capability point of view, it has been proven over and over again that test quality not only does not suffer but can exceed that of a vertical supply chain. Yet what about cost (as in cost of test)? Are there still inefficiencies we can look at and improve on? In this session, we will discuss our perspectives, a semiconductor test equipment supplier on what we as an industry must do differently, how we must innovate in order for this horizontal supply chain to match the efficiencies and most importantly, the cost derived from a vertically integrated supply chain.